

Low Resistivity and High Breakdown Current Density of 10-nm Diameter van der Waals TaSe₃ Nanowires by Chemical Vapor Deposition

Thomas A. Empante¹, Aimee Martinez¹, Michelle Wurch¹, Yanbing Zhu², Adane K. Geremew³, Koichi Yamaguchi¹, Miguel Isarraraz¹, Sergey Rumyantsev^{3,4}, Evan J. Reed², Alexander A. Balandin³, Ludwig Bartels¹

¹Department of Chemistry and Material Science & Engineering Program, University of California – Riverside, Riverside, CA 92521, United States

²Department of Materials Science and Engineering, Stanford University, Stanford, California 94304, United States

³Nano-Device Laboratory, Department of Electrical and Computer Engineering, University of California, Riverside, California 92521, United States

⁴Center for Terahertz Research and Applications, Institute of High Pressure Physics, Polish Academy of Sciences, Warsaw 01-142, Poland

Supporting Information

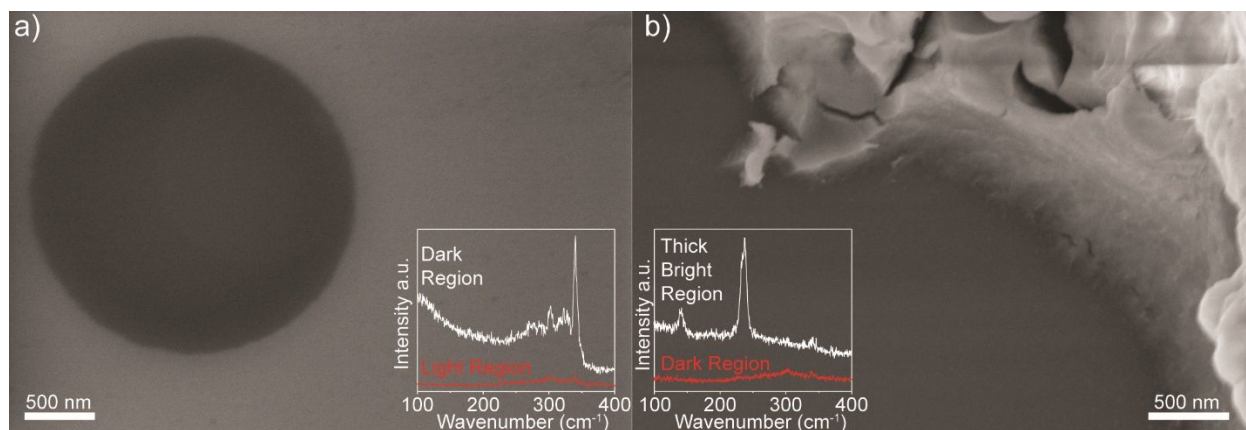


Figure S1: Scanning electron microscopy images and Raman spectra (on and off precipitate) for drops of the following solutions dried on a 300nm SiO₂/Si substrate: a) TaCl₅ mixed with diethyl ether and b) TaCl₅ mixed with diethyl ether and dissolved in ethanol. Both a) and b) lack any nanowire structures in the SEM. Neither of the precipitates exhibit the Raman signature seen for the TaSe₃ nanowires. The Raman peaks at 141 and 237cm⁻¹ after addition of both diethyl ether and ethanol, b), resemble those of 2H TaSe₂.

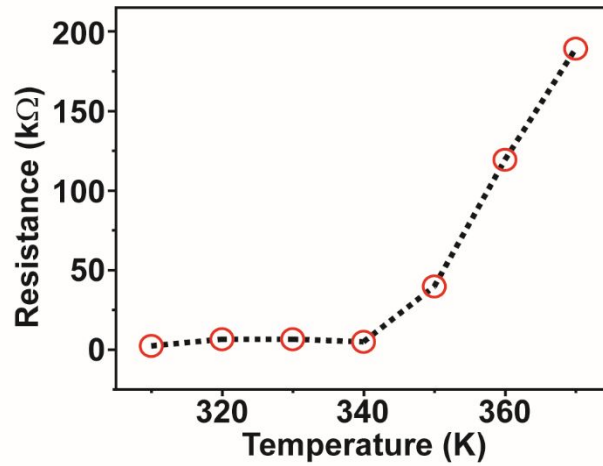


Figure S2: Resistance of a CVD 1D vdW TaSe₃ nanowire bundle as a function of temperature. The resistivity increases with temperature, similar to other metals. The gradual and relatively slow resistance increase below 350 K is due to increasing electron–phonon scattering. The sharp increase in resistance at temperatures above 350 K is likely related to the onset of electromigration as commonly observed in conventional interconnect reliability tests.^{1–4} The slope may be enhanced by local self-heating due to the small wire bundle cross section area and low thermal conductivity of the SiO₂ layer under the nanowire and PMMA film encapsulating it.

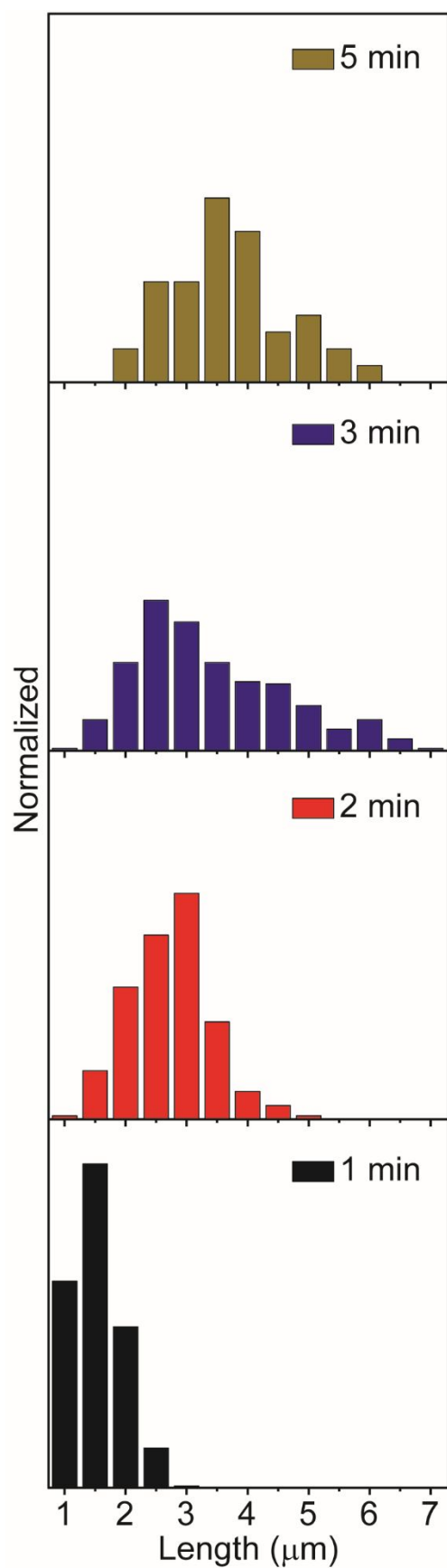


Figure S3: Distribution of the TaSe₃ nanowire's length as a function of hold time at 400°C. As the hold is prolonged, wires grow longer and short wires deteriorate.

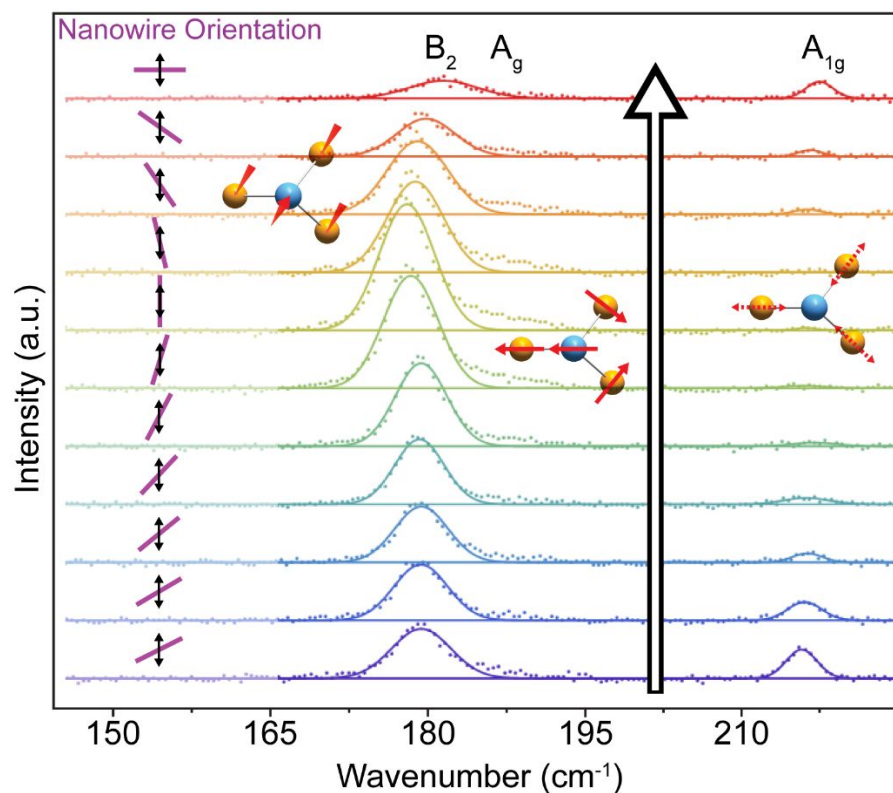


Figure S4: Dependence of the Raman spectrum of TaSe₃ on the angle between the plane of polarization (\updownarrow) and the nanowire (purple line). The dots represent measurements, the solid lines are Gaussian fits to the peaks. Parallel alignment shows a mode at 178-180 cm⁻¹ whereas in parallel alignment and additional mode at 215-217 cm⁻¹ is most pronounced. The spectra were acquired from bottom to top (vertical arrow); slight degradation of the nanowire over time is visible in the high-energy shoulder of the 178-180 cm⁻¹ peak and reduced overall signal.

Computational modeling reveals near 178 cm⁻¹ two modes, one associated with longitudinal wagging motion of the Ta atoms relative to the Se trimers (B₂) and one associated with a scissoring of two selenium atoms about the third and the tantalum atom (A_g). At about 216 cm⁻¹ three modes are found that represent symmetric and antisymmetric stretching motion of the Se trimer about the Ta atom (A_{1g}). The direction of displacement of the atoms in these modes matches the polarization dependence of the Raman signal.

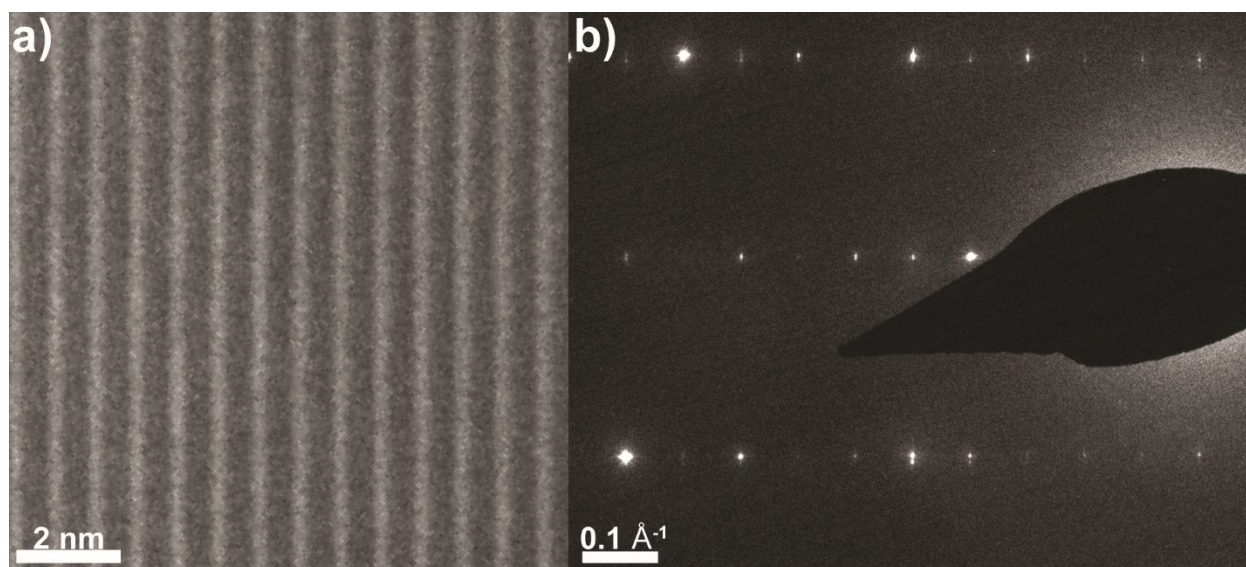


Figure S5: Representative scanning transmission electron microscopy (STEM) image and diffraction pattern of a TaSe₃ nanowire.

References:

1. Beyne, S.; Croes, K.; De Wolf, I.; Tokei, Z., 1/f noise measurements for faster evaluation of electromigration in advanced microelectronics interconnections. *Journal of Applied Physics* **2016**, 119, (18), 184302.
2. Bagnoli, P. E.; Ciofi, C.; Neri, B.; Pennelli, G., Electromigration in Al based stripes: Low frequency noise measurements and MTF tests. *Microelectronics and Reliability* **1996**, 36, (7-8), 1045-1050.
3. Koch, R. H.; Lloyd, J. R.; Cronin, J., 1/F Noise and Grain-Boundary Diffusion in Aluminum and Aluminum-Alloys. *Physical Review Letters* **1985**, 55, (22), 2487-2490.
4. Chen, T. M.; Yassine, A. M., Electrical Noise and Vlsi Interconnect Reliability. *Ieee Transactions on Electron Devices* **1994**, 41, (11), 2165-2172.